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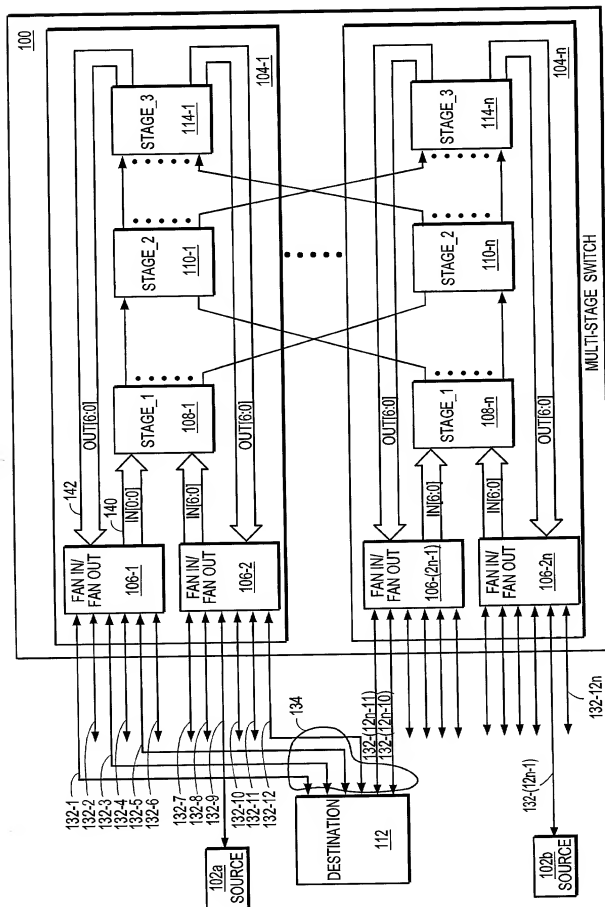
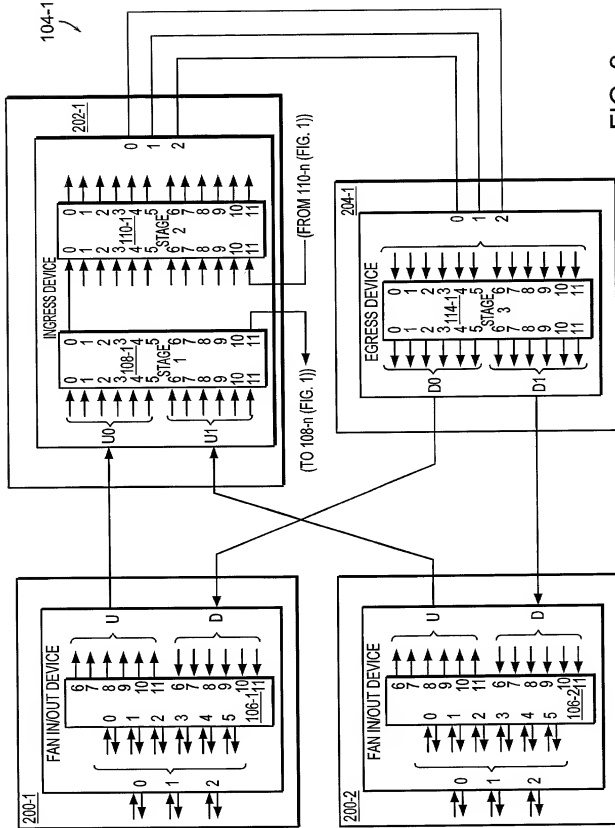


FIG. 1



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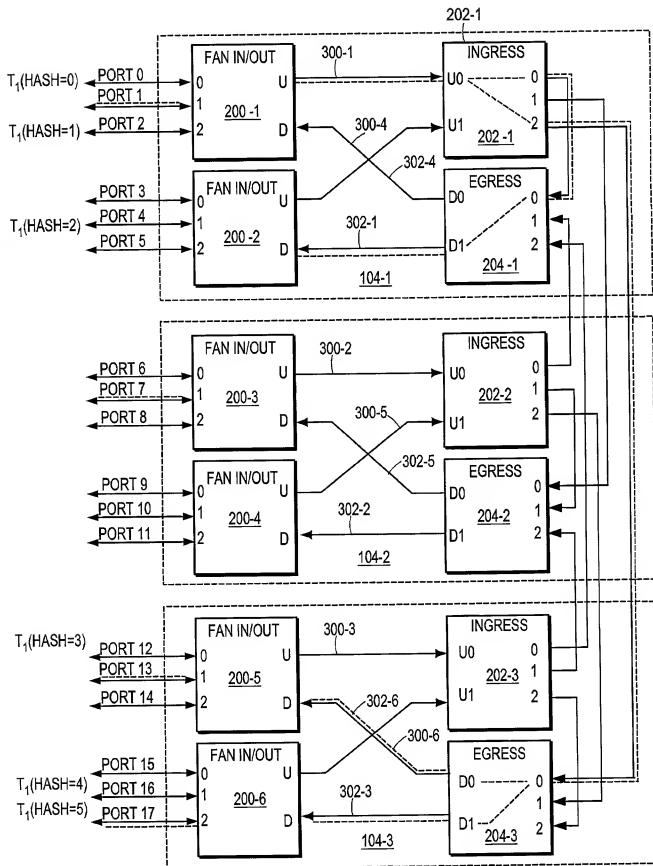
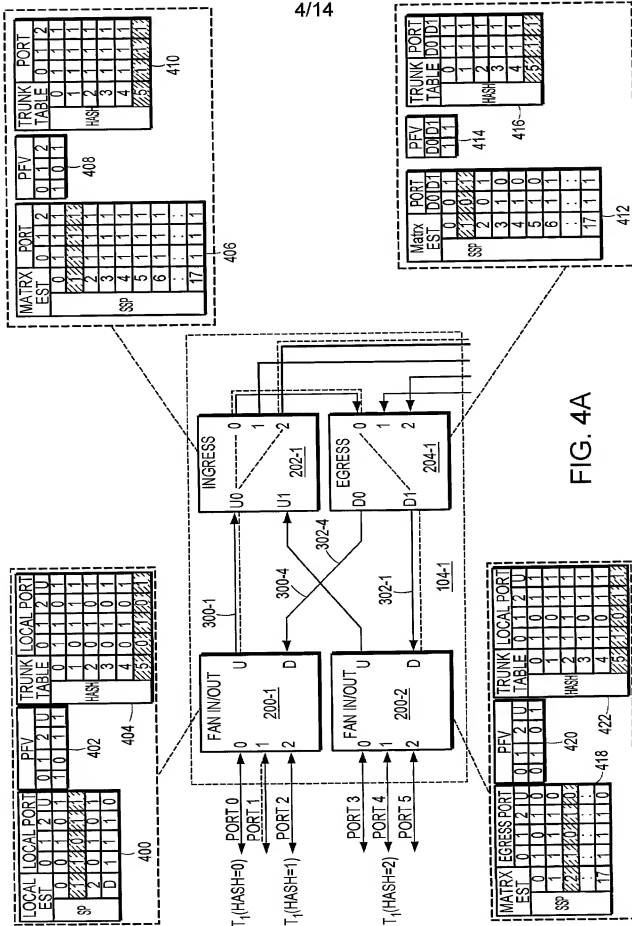


FIG. 3



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MATRIX EGRESS PORT			PFV			TRUNK LOCAL PORT		
EST	0	1	2	U	0	1	2	U
	0	0	1	1	0	1	0	1
	1	1	1	1	0	0	1	1
	2	0	1	1	0	0	1	1
	3	1	1	1	1	0	1	1
	4	0	1	1	1	1	0	1
	5	0	1	1	1	1	1	0
	6	1	1	1	1	1	1	0
	7	1	1	1	1	1	1	0
	8	1	1	1	1	1	1	0
	9	1	1	1	1	1	1	0
	10	1	1	1	1	1	1	0
	11	1	1	1	1	1	1	0
	12	1	1	1	1	1	1	0
	13	1	1	1	1	1	1	0
	14	1	1	1	1	1	1	0
	15	1	1	1	1	1	1	0
	16	1	1	1	1	1	1	0
	17	1	1	1	1	1	1	0
	18	1	1	1	1	1	1	0
	19	1	1	1	1	1	1	0
	20	1	1	1	1	1	1	0
	21	1	1	1	1	1	1	0
	22	1	1	1	1	1	1	0
	23	1	1	1	1	1	1	0
	24	1	1	1	1	1	1	0
	25	1	1	1	1	1	1	0
	26	1	1	1	1	1	1	0
	27	1	1	1	1	1	1	0
	28	1	1	1	1	1	1	0
	29	1	1	1	1	1	1	0
	30	1	1	1	1	1	1	0
	31	1	1	1	1	1	1	0
	32	1	1	1	1	1	1	0
	33	1	1	1	1	1	1	0
	34	1	1	1	1	1	1	0
	35	1	1	1	1	1	1	0
	36	1	1	1	1	1	1	0
	37	1	1	1	1	1	1	0
	38	1	1	1	1	1	1	0
	39	1	1	1	1	1	1	0
	40	1	1	1	1	1	1	0
	41	1	1	1	1	1	1	0
	42	1	1	1	1	1	1	0
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	44	1	1	1	1	1	1	0
	45	1	1	1	1	1	1	0
	46	1	1	1	1	1	1	0
	47	1	1	1	1	1	1	0
	48	1	1	1	1	1	1	0
	49	1	1	1	1	1	1	0
	50	1	1	1	1	1	1	0
	51	1	1	1	1	1	1	0
	52	1	1	1	1	1	1	0
	53	1	1	1	1	1	1	0
	54	1	1	1	1	1	1	0
	55	1	1	1	1	1	1	0
	56	1	1	1	1	1	1	0
	57	1	1	1	1	1	1	0
	58	1	1	1	1	1	1	0
	59	1	1	1	1	1	1	0
	60	1	1	1	1	1	1	0
	61	1	1	1	1	1	1	0
	62	1	1	1	1	1	1	0
	63	1	1	1	1	1	1	0
	64	1	1	1	1	1	1	0
	65	1	1	1	1	1	1	0
	66	1	1	1	1	1	1	0
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	68	1	1	1	1	1	1	0
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	70	1	1	1	1	1	1	0
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	72	1	1	1	1	1	1	0
	73	1	1	1	1	1	1	0
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	79	1	1	1	1	1	1	0
	80	1	1	1	1	1	1	0
	81	1	1	1	1	1	1	0
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	88	1	1	1	1	1	1	0
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	91	1	1	1	1	1	1	0
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	96	1	1	1	1	1	1	0
	97	1	1	1	1	1	1	0
	98	1	1	1	1	1	1	0
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	100	1	1	1	1	1	1	0
	101	1	1	1	1	1	1	0
	102	1	1	1	1	1	1	0
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	105	1	1	1	1	1	1	0
	106	1	1	1	1	1	1	0
	107	1	1	1	1	1	1	0
	108	1	1	1	1	1	1	0
	109	1	1	1	1	1	1	0
	110	1	1	1	1	1	1	0
	111	1	1	1	1	1	1	0
	112	1	1	1	1	1	1	0
	113	1	1	1	1	1	1	0
	114	1	1	1	1	1	1	0
	115	1	1	1	1	1	1	0
	116	1	1	1	1	1	1	0
	117	1	1	1	1	1	1	0
	118	1	1	1	1	1	1	0
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	120	1	1	1	1	1	1	0
	121	1	1	1	1	1	1	0
	122	1	1	1	1	1	1	0
	123	1	1	1	1	1	1	0
	124	1	1	1	1	1	1	0
	125	1	1	1	1	1	1	0
	126	1	1	1	1	1	1	0
	127	1	1	1	1	1	1	0
	128	1	1	1	1	1	1	0
	129	1	1	1	1	1	1	0
	130	1	1	1	1	1	1	0
	131	1	1	1	1	1	1	0
	132	1	1	1	1	1	1	0
	133	1	1	1	1	1	1	0
	134	1	1	1	1	1	1	0
	135	1	1	1	1	1	1	0
	136	1	1	1	1	1	1	0
	137	1	1	1	1	1	1	0
	138	1	1	1	1	1	1	0
	139	1	1	1	1	1	1	0
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	143	1	1	1	1	1	1	0
	144	1	1	1	1	1	1	0
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	154	1	1	1	1	1	1	0
	155	1	1	1	1	1	1	0
	156	1	1	1	1	1	1	0
	157	1	1	1	1	1	1	0
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	159	1	1	1	1	1	1	0
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	161	1	1	1	1	1	1	0
	162	1	1	1	1	1	1	0
	163	1	1	1	1	1	1	0
	164	1	1	1	1	1	1	0
	165	1	1	1	1	1	1	0
	166	1	1	1	1	1	1	0
	167	1	1	1	1	1	1	0
	168	1	1	1	1	1	1	0
	169	1	1	1	1	1	1	0
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	182	1	1	1	1	1	1	0
	183	1	1	1	1	1	1	0
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	185	1	1	1	1	1	1	0
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	197	1	1	1	1	1	1	0
	198	1	1	1	1	1	1	0
	199	1	1	1	1	1	1	0
	200	1	1	1	1	1	1	0
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	202	1	1	1	1	1	1	0

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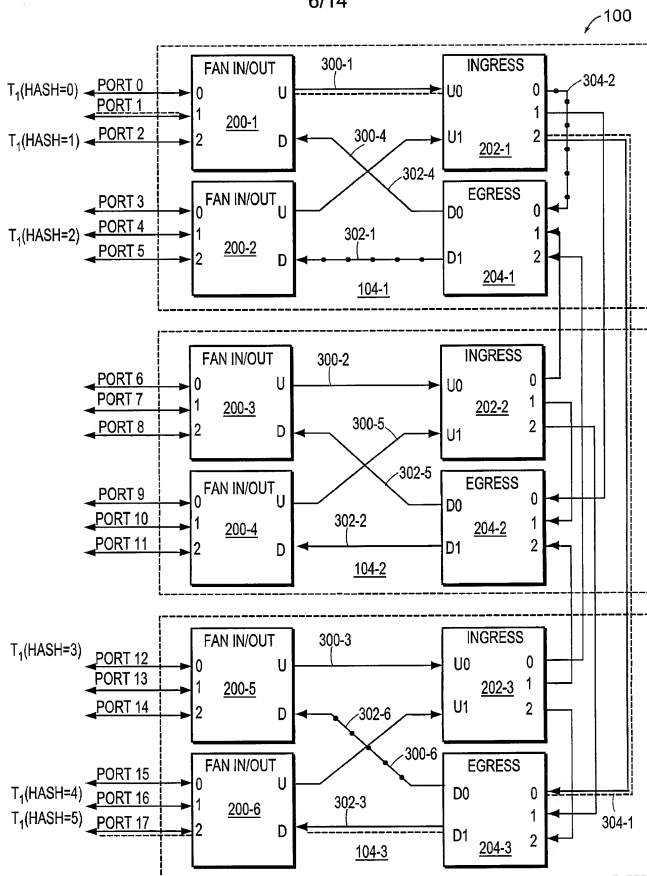


FIG. 5

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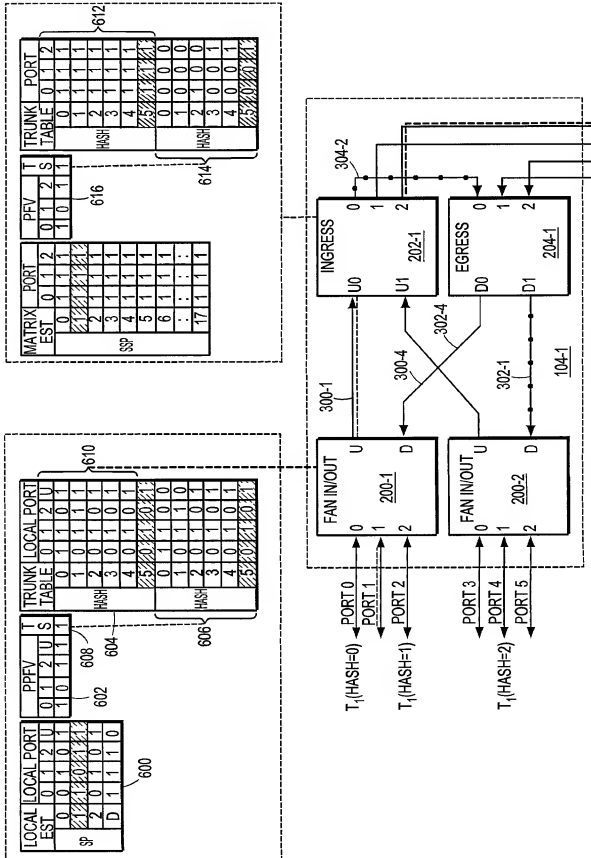


FIG. 6A

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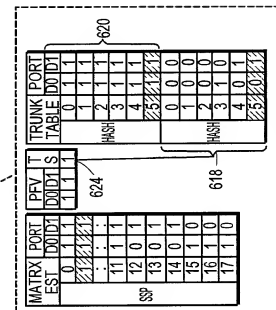
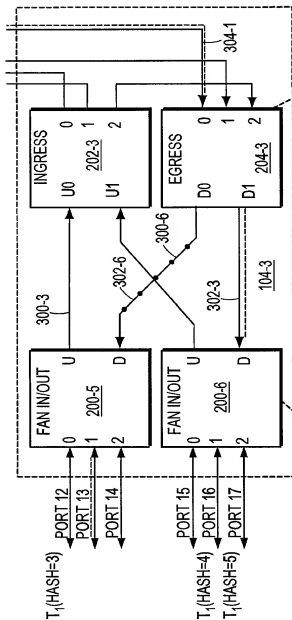
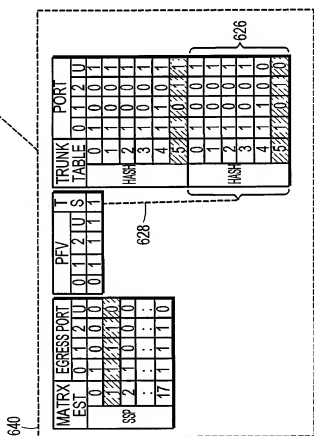
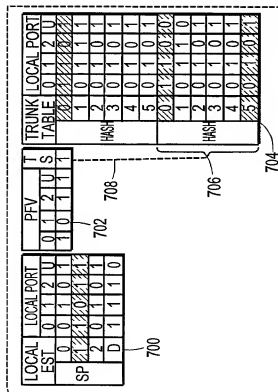


FIG. 6B





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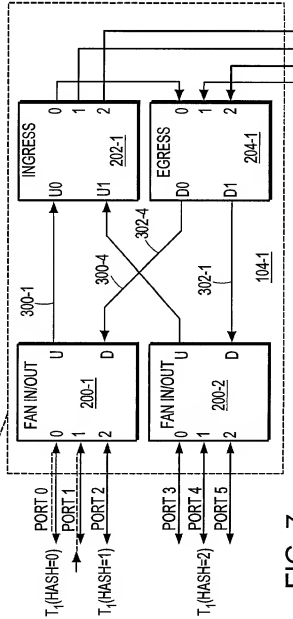
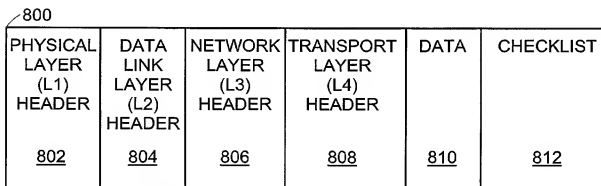
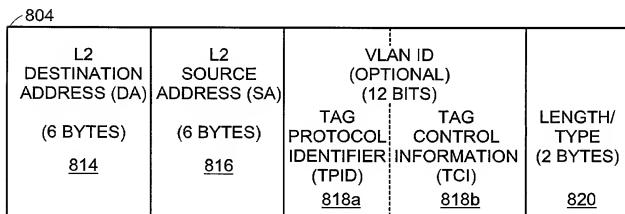


FIG. 7

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PRIOR ART
 FIG. 8A



PRIOR ART
 FIG. 8B

10066550.042602

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<u>822</u>	<u>834</u>	<u>836</u>	<u>828</u>
VERS	HLEN	TOS	TOTAL LENGTH
<u>830</u>		<u>832</u>	<u>834</u>
IDENTIFICATION		FLAGS	FRAGMENT OFFSET
<u>836</u>	<u>840</u>	<u>842</u>	
TTL	PROTOCOL	HEADER CHECKSUM	
IP SOURCE ADDRESS			<u>844</u>
IP DESTINATION ADDRESS			<u>846</u>
<u>848</u>			<u>850</u>
OPTIONS			PAD

PRIOR ART
 FIG. 8C

20240925001

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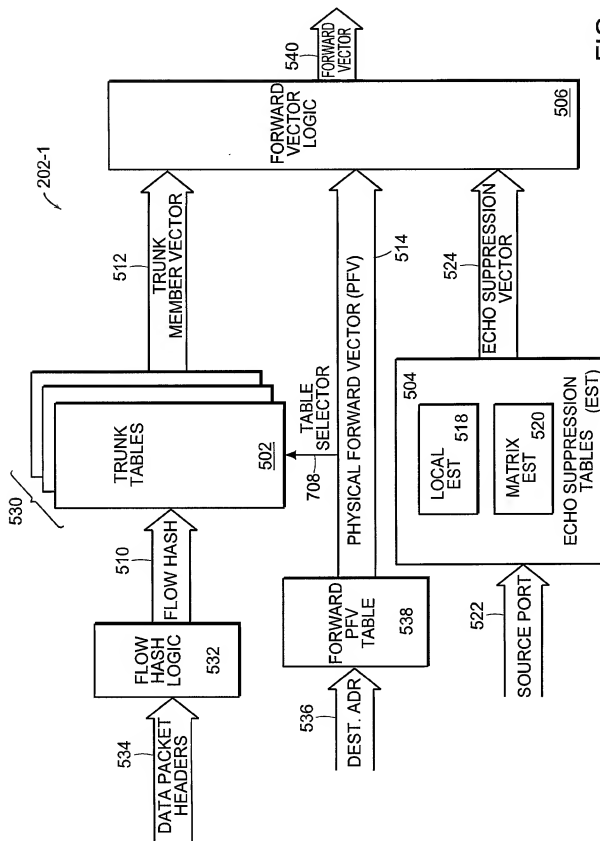


FIG. 9

209240-05599001

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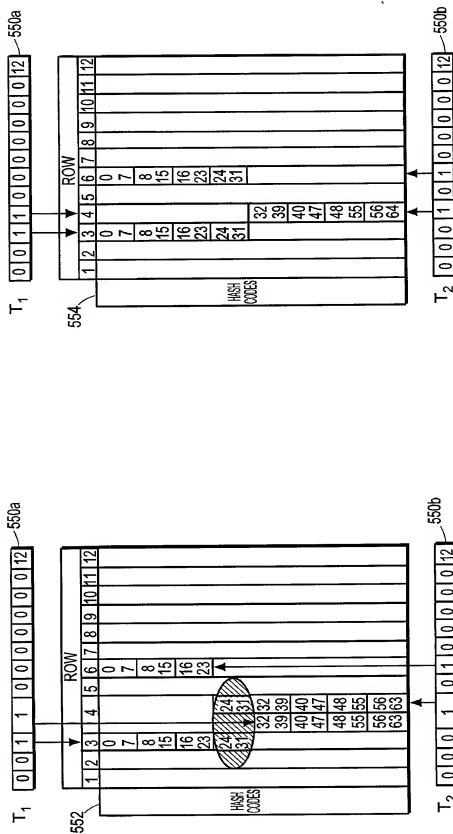


FIG. 10B

FIG. 10A

